

AMENDMENTS TO THE CLAIMS:

The listing of claims will replace all prior versions, and listings of claims in the application:

LISTING OF THE CLAIMS

1. (Currently Amended) A switchless digital communication system for processing at least one of cell and packet information, the switchless digital communication system comprising:

at least one node interconnected through a fabric, the at least one node comprising:

at least one of a plurality of network processing devices for receiving at least one of the cell and the packet information, for determining a destination within the node for the cell and the packet information, and for at least one of routing and forwarding the cell and the packet information to the destination;

a shared bus structure for coupling each of the network processing devices with each other; and

an interface for coupling at least one of the network processing devices with the fabric to support communication between nodes, where the fabric is in direct connection with the other nodes.

2. (Original) The digital communication system of claim 1, wherein the destination is determined in response to at least one of stored routing rules and characteristics of the cell and the packet information.

3. (Original) The digital communication system of claim 2, wherein the at least one of a plurality of network processing devices employ dynamically updated routing rules.

4. (Original) The digital communication system of claim 1, wherein the at least one of a plurality of network processing devices performs the at least one of

routing and forwarding on both the cell and the packet information simultaneously.

5. (Original) The digital communication system of claim 1, wherein the at least one of a plurality of network processing devices directly delivers the at least one of routing and forwarding the cell and the packet information into a memory of the destination.

6. (Original) The digital communication system of claim 1, wherein the at least one network processing device supports peer-to-peer routing.

7. (Original) The digital communication system of claim 1, wherein the interface provides the cell and the packet information to the at least one network processing device.

8. (Original) The digital communication system of claim 7, wherein the interface comprises at least one of a System Interface and a Maintenance Interface.

9. (Original) The digital communication system of claim 7, wherein the interface comprises a multiplexer for creating a multiplexed stream from the at least one of the cell and the packet information.

10. (Original) The digital communication system of claim 9, wherein the multiplexed stream is received through at least one of a System Interface and a Maintenance Interface.

11. (Original) The digital communication system of claim 1, wherein the node further comprises:

a general-purpose processor for at least one of controlling the at least two network processing devices and performing maintenance on the node.

12. (Original) The digital communication system of claim 11, wherein the shared bus structure couples the general-purpose processor with each of the network processing devices.
13. (Original) The digital communication system of claim 12, wherein the shared bus structure comprises a Peripheral Component Interconnect bus.
14. (Original) The digital communication system of claim 11, wherein the general-purpose processor supports peer-to-peer routing with at least one of the network processing devices.
15. (Original) The digital communication system of claim 1, comprising:
at least one external system input/output interface.
16. (Original) The digital communication system of claim 15, wherein the external system input/output interface supports at least one transport mechanism type, the at least one transport mechanism type comprising at least one of Asynchronous Transfer Mode, Internet Protocol, and Frame Relay.
17. (Currently Amended) A communication node for processing at least one of cell and packet information comprising:
at least one of a plurality of network processing devices for receiving at least one of the cell and the packet information, for determining a destination within the node for the cell and the packet information, and for at least one of routing and forwarding the cell and the packet information to the destination, the destination determined in response to at least one of stored routing rules and characteristics of the cell and the packet information;
a shared bus structure for coupling each of the network processing devices with each other; and
at least one of a System Interface and a Maintenance Interface for coupling for providing the cell and the packet information to the at least one

network processing device where the at least one of a System Interface and a Maintenance Interface directly connects to a switchless fabric.

18. (Original) The communication node of claim 17, wherein the at least one of a plurality of network processing devices employ dynamically updated routing rules.

19. (Original) The communication node of claim 17, wherein the at least one of a plurality of network processing devices performs the at least one of routing and forwarding on both the cell and the packet information simultaneously.

20. (Original) The communication node of claim 17, wherein the at least one of a plurality of network processing devices directly delivers the at least one of routing and forwarding the cell and the packet information into a memory of the destination.

21. (Original) The communication node of claim 17, wherein the at least one network processing device supports peer-to-peer routing.

22. (Original) The communication node of claim 17, comprising:
a multiplexer for creating a multiplexed stream from the at least one of the cell and the packet information, the multiplexed stream is received through at least one of a System Interface and a Maintenance Interface.

23. (Original) The communication node of claim 17, comprising:
a general-purpose processor for controlling the at least two network processing devices, wherein the shared bus structure couples the general-purpose processor with each of the network processing devices.

24. (Original) The communication node of claim 17, wherein the shared bus structure couples the general-purpose processor with each of the network

processing devices.

25. (Original) The communication node of claim 17, wherein the shared bus structure comprises a Peripheral Component Interconnect bus.

26. (Original) The communication node of claim 17, comprising:

at least one external system input/output interface supportive of at least one transport mechanism type, the at least one transport mechanism type comprising Asynchronous Transfer Mode, Internet Protocol, and Frame Relay.